# **General Purpose Transistor**

# **NPN Silicon**

#### **Features**

- Moisture Sensitivity Level: 1
- Pb-Free Package is Available

#### **MAXIMUM RATINGS**

Rating	Symbol	Value	Unit	
Collector-Emitter Voltage	Voltage V <sub>CEO</sub>			
Collector-Base Voltage	V <sub>CBO</sub>	75	Vdc	
Emitter-Base Voltage	V <sub>EBO</sub>	6.0	Vdc	
Collector Current - Continuous	I <sub>C</sub>	600	mAdc	
Electrostatic Discharge	ESD	HBM Class 2 MM Class B		

#### THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Total Package Dissipation (Note 1), T <sub>A</sub> = 25°C	P <sub>D</sub>	150	mW
Thermal Resistance, Junction-to-Ambient	$R_{ heta JA}$	833	°C/W
Junction and Storage Temperature	T <sub>J</sub> , T <sub>stg</sub>	-55 to +150	°C

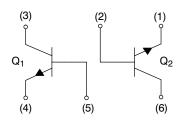
Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

1. Device mounted on FR4 glass epoxy printed circuit board using the minimum recommended footprint.



#### ON Semiconductor®

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SC-88/SC70-6/SOT-363 CASE 419B STYLE 1

#### **MARKING DIAGRAM**



1P = Specific Device Code

M = Date Code ■ Pb-Free Package

(Note: Microdot may be in either location)

#### **ORDERING INFORMATION**

Device	Package	Shipping <sup>†</sup>
MBT2222ADW1T1	SOT-363	3000/Tape & Reel
MBT2222ADW1T1G	SOT-363 (Pb-Free)	3000/Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

# **ELECTRICAL CHARACTERISTICS** ( $T_A = 25$ °C unless otherwise noted)

Ch	paracteristic	Symbol	Min	Max	Unit	
OFF CHARACTERISTICS				•	•	
Collector-Emitter Breakdown Voltage	$(I_C = 10 \text{ mAdc}, I_B = 0)$	V <sub>(BR)CEO</sub>	40	-	Vdc	
Collector-Base Breakdown Voltage	$(I_{C} = 10 \mu Adc, I_{E} = 0)$	V <sub>(BR)CBO</sub>	75	-	Vdc	
Emitter-Base Breakdown Voltage,	$(I_E = 10 \mu Adc, I_C = 0)$	V <sub>(BR)EBO</sub>	6.0	-	Vdc	
Collector Cutoff Current	(V <sub>CE</sub> = 60 Vdc, V <sub>EB(off)</sub> = 3.0 Vdc)	I <sub>CEX</sub>	-	10	nAdc	
Collector Cutoff Current	(V <sub>CB</sub> = 60 Vdc, I <sub>E</sub> = 0) (V <sub>CB</sub> = 60 Vdc, I <sub>E</sub> = 0, T <sub>A</sub> = 125°C)	I <sub>CBO</sub>		0.01 10	μAdc	
Emitter Cutoff Current	$(V_{EB} = 3.0 \text{ Vdc}, I_{C} = 0)$	I <sub>EBO</sub>	-	100	nAdc	
Base Cutoff Current	(V <sub>CE</sub> = 60 Vdc, V <sub>EB(off)</sub> = 3.0 Vdc)	I <sub>BL</sub>	-	20	nAdc	
ON CHARACTERISTICS	` ,		1			
DC Current Gain	$ \begin{array}{c} (I_C=0.1 \text{ mAdc, } V_{CE}=10 \text{ Vdc}) \\ (I_C=1.0 \text{ mAdc, } V_{CE}=10 \text{ Vdc}) \\ (I_C=10 \text{ mAdc, } V_{CE}=10 \text{ Vdc}) \\ (I_C=10 \text{ mAdc, } V_{CE}=10 \text{ Vdc, } T_{A}=-55^{\circ}\text{C}) \\ (I_C=150 \text{ mAdc, } V_{CE}=10 \text{ Vdc) (Note 2)} \\ (I_C=150 \text{ mAdc, } V_{CE}=1.0 \text{ Vdc) (Note 2)} \\ (I_C=500 \text{ mAdc, } V_{CE}=10 \text{ Vdc) (Note 2)} \\ \end{array} $	h <sub>FE</sub>	35 50 75 35 100 50 40	- - - - 300 -	-	
Collector-Emitter Saturation Voltage (N	Note 2) $ \begin{aligned} (I_{C} &= 150 \text{ mAdc}, \ I_{B} = 15 \text{ mAdc}) \\ (I_{C} &= 500 \text{ mAdc}, \ I_{B} = 50 \text{ mAdc}) \end{aligned} $	V <sub>CE(sat)</sub>		0.3 1.0	Vdc	
Base-Emitter Saturation Voltage (Note	V <sub>BE(sat)</sub>	0.6	1.2 2.0	Vdc		
SMALL-SIGNAL CHARACTERISTIC	s					
Current-Gain - Bandwidth Product (No	ote 3) (I <sub>C</sub> = 20 mAdc, V <sub>CE</sub> = 20 Vdc, f = 100 MHz)	f <sub>T</sub>	300	-	MHz	
Output Capacitance	(V <sub>CB</sub> = 10 Vdc, I <sub>E</sub> = 0, f = 1.0 MHz)	C <sub>obo</sub>	-	8.0	pF	
Input Capacitance	$(V_{EB} = 0.5 \text{ Vdc}, I_{C} = 0, f = 1.0 \text{ MHz})$	C <sub>ibo</sub>	-	25	pF	
Input Impedance	$(I_C = 1.0 \text{ mAdc}, V_{CE} = 10 \text{ Vdc}, f = 1.0 \text{ kHz})$ $(I_C = 10 \text{ mAdc}, V_{CE} = 10 \text{ Vdc}, f = 1.0 \text{ kHz})$	h <sub>ie</sub>	2.0 0.25	8.0 1.25	kΩ	
Voltage Feedback Ratio	$(I_{C} = 1.0 \text{ mAdc}, V_{CE} = 10 \text{ Vdc}, f = 1.0 \text{ kHz})$ $(I_{C} = 10 \text{ mAdc}, V_{CE} = 10 \text{ Vdc}, f = 1.0 \text{ kHz})$	h <sub>re</sub>	-	8.0 4.0	X 10-	
Small-Signal Current Gain	$(I_{C} = 1.0 \text{ mAdc}, V_{CE} = 10 \text{ Vdc}, f = 1.0 \text{ kHz})$ $(I_{C} = 10 \text{ mAdc}, V_{CE} = 10 \text{ Vdc}, f = 1.0 \text{ kHz})$	h <sub>fe</sub>	50 75	300 375	-	
Output Admittance	$(I_C = 1.0 \text{ mAdc}, V_{CE} = 10 \text{ Vdc}, f = 1.0 \text{ kHz})$ $(I_C = 10 \text{ mAdc}, V_{CE} = 10 \text{ Vdc}, f = 1.0 \text{ kHz})$	h <sub>oe</sub>	5.0 25	35 200	μmhos	
Collector Base Time Constant	$(I_E = 20 \text{ mAdc}, V_{CB} = 20 \text{ Vdc}, f = 31.8 \text{ MHz})$	rb, C <sub>c</sub>	-	150	ps	
Noise Figure (I <sub>C</sub> = 100	NF	-	4.0	dB		
SWITCHING CHARACTERISTICS						
Delay Time	(V <sub>CC</sub> = 30 Vdc, V <sub>BE(off)</sub> = -0.5 Vdc,	t <sub>d</sub>	-	10		
Rise Time	I <sub>C</sub> = 150 mAdc, I <sub>B1</sub> = 15 mAdc)	t <sub>r</sub>	-	25	ns	
Ctaraga Tima	0.4	t <sub>s</sub>	-	225	1	
Storage Time	$(V_{CC} = 30 \text{ Vdc}, I_{C} = 150 \text{ mAdc},$	*5		220	ns	

<sup>2.</sup> Pulse Test: Pulse Width  $\leq$  300  $\mu$ s, Duty Cycle  $\leq$  2.0%. 3. f<sub>T</sub> is defined as the frequency at which  $|h_{fe}|$  extrapolates to unity.

#### **SWITCHING TIME EQUIVALENT TEST CIRCUITS**

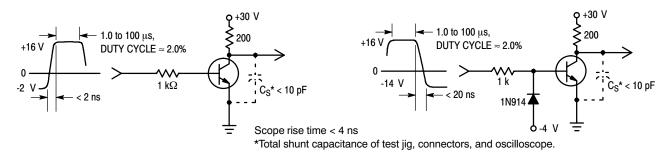


Figure 1. Turn-On Time

Figure 2. Turn-Off Time

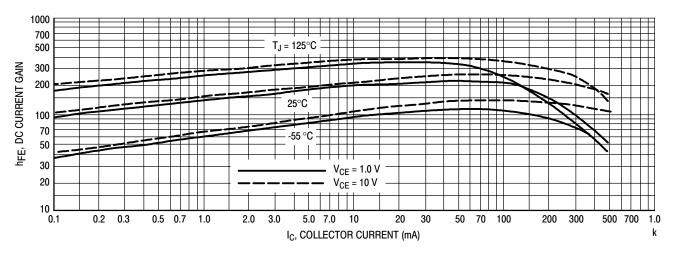


Figure 3. DC Current Gain

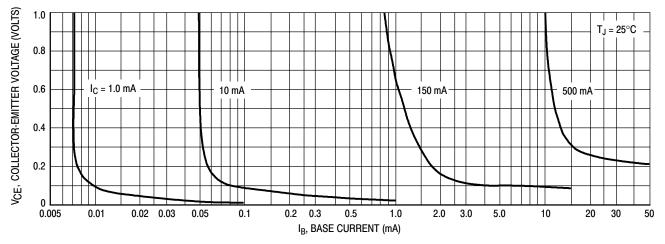
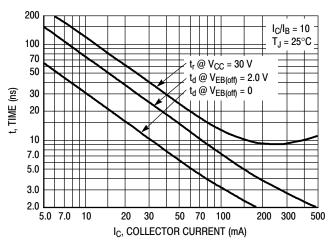


Figure 4. Collector Saturation Region

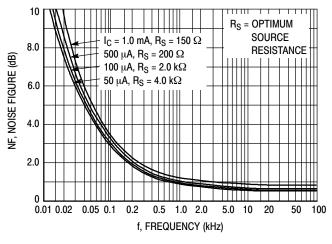
500



 $V_{CC} = 30 \text{ V}$ 300  $I_C/I_B = 10$  $t'_{s} = t_{s} - 1/8 t_{f}$ 200  $I_{B1} = I_{B2}$  $T_J = 25^{\circ}C$ 100 t, TIME (ns) 70 50 30 20 10 7.0 5.0 5.0 7.0 10 50 70 100 200 300 500 IC, COLLECTOR CURRENT (mA)

Figure 5. Turn-On Time

Figure 6. Turn-Off Time



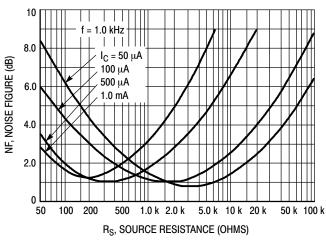
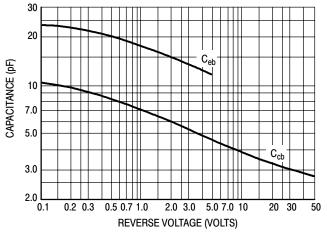


Figure 7. Frequency Effects

**Figure 8. Source Resistance Effects** 



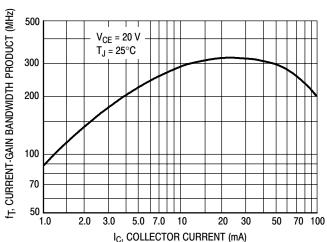
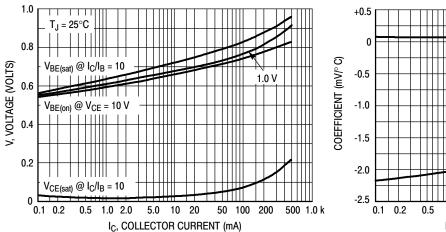
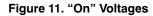
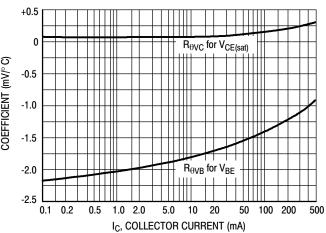


Figure 9. Capacitances

Figure 10. Current-Gain Bandwidth Product





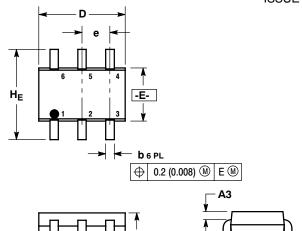


**Figure 12. Temperature Coefficients** 

#### PACKAGE DIMENSIONS

#### SC-88/SC70-6/SOT-363

CASE 419B-02 **ISSUE W** 



- NOTES:
  1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982. CONTROLLING DIMENSION: INCH.
- 419B-01 OBSOLETE, NEW STANDARD 419B-02.

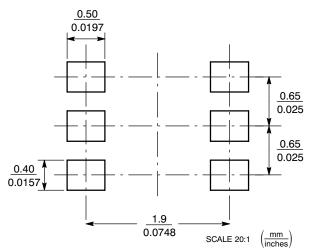
	MILLIMETERS			INCHES			
DIM	MIN	NOM	MAX	MIN	NOM	MAX	
Α	0.80	0.95	1.10	0.031	0.037	0.043	
A1	0.00	0.05	0.10	0.000	0.002	0.004	
АЗ	0.20 REF			(	0.008 REF		
b	0.10	0.21	0.30	0.004	0.008	0.012	
С	0.10	0.14	0.25	0.004	0.005	0.010	
D	1.80	2.00	2.20	0.070	0.078	0.086	
E	1.15	1.25	1.35	0.045	0.049	0.053	
е	0.65 BSC			0	.026 BS	С	
L	0.10	0.20	0.30	0.004	0.008	0.012	
He	2 00	2 10	2.20	0.078	0.082	0.086	

STYLE 1:

- PIN 1. EMITTER 2 2. BASE 2

  - 3. COLLECTOR 1 4. EMITTER 1

  - BASE 1 COLLECTOR 2
- **SOLDERING FOOTPRINT\***



\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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